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7/ Election
Hawkins
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Via Facsimile to 703-872-9318 Date of Facsimile: September 5, 2003
I hereby certify that this paper or fee is being faxed to the U.S. Patent Office on the date indicated above (1 page total)

Attn: Examiner Jean F. Duverne

Rhonda Dunn
Rhonda Dunn

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mok, et al.

Docket No.: NNEX0003

Serial No. : 09/980,040

Art Unit: 2839

Filed: 11/27/2001

Examiner: Duverne, Jean

Title: Construction Structures and Manufacturing Processes for Integrated Circuit Wafer Probe Card Assemblies

September 5, 2003

Commissioner for Patents
Attn: Examiner Jean F. Duverne
Mail Stop Non Fee Amendment
P.O. Box 1450
Alexandria, VA 22313-1450

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RESPONSE

Sir:

This Amendment is responsive to the Office Action, dated August 5, 2003, for the above-identified patent application.

Election

Applicant hereby elects without traverse Group 1, Claims 1-22, to be examined in the above-identified application.

Respectfully Submitted,

Michael A. Glenn

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